



ISO 9001: 2015  
ISO 14001: 2015  
Certified

**CENTRAL ELECTRONICS LIMITED**  
(A Government of India Enterprise)  
Under Department of Scientific & Industrial Research  
Ministry of Science & Technology  
4, Industrial Area, Sahibabad, Ghaziabad (UP)  
Tel.No.0120-2895143, E-mail: [celrecruitment@celindia.co.in](mailto:celrecruitment@celindia.co.in)  
**CIN : U32109DL1974GOI007325**

Central Electronics Limited is a Govt. of India Enterprise under Department of Scientific & Industrial Research (DSIR), Ministry of Science & Technology. It was established in 1974 with an objective to commercially exploit the indigenous technologies developed by National Laboratories and R&D Institutions in the country. CEL has developed a number of products for the first time in the country through its own R&D efforts and in close association with the premier National & International Laboratories including Defence Laboratories.

Currently, CEL operates in four prime business verticals, viz. Solar Photovoltaic (SPV), Railway Signaling System, Strategic Electronics, Security and Surveillance Group (SSG). Based on short term and long term goals of CEL the aim is to increase market presence across multiple regions in India and to have a quantum leap in turnover and profit of the company in the coming years. In order to achieve the same, we need highly skilled, experienced and work oriented individuals. Accordingly, CEL invites application from Indian nationals with matching skills, experience and endurance for the following posts:

S. No	Post	Roles and Responsibilities	Qualification & Experience
1	<b>Sr. Manager (HR)/ Manager (HR)</b>  Grade – E4/E3 Post–02 01 (UR) 01 (OBC)	The candidate shall be responsible for Recruitment, Industrial Relations, Manpower Planning, Training & Development, Establishment, Legal and Welfare matters.	The candidate should be a Graduate along with MBA/PGP/PGDM (02 Years) in Personnel Management/Human Resource Management or equivalent qualification such as 02 years degree/diploma in Labour Welfare/ Industrial Relations /Social Work etc. with minimum 55% marks from a recognized Institute/ University.  He/She should have <b>minimum 12 years</b> of post qualification experience in HR/IR functions.  Degree in Law is desirable.  Candidate with 09 years of post qualification experience shall be eligible for Manager (HR).
2	<b>Assistant Technical Manager (Microwave)</b>  Grade – E2 Post–02 01 (UR) 01 (OBC)	The candidate shall be responsible for the following:  (i) Primarily for Development, assembly & testing of microwave components, sub-system & system for commercial production.  (ii) The candidate should also have ability to develop RF engineering design.  (iii) Candidate will also be part of a team for Setting-up of assembly (SMT line /RF assembly line) & testing	The candidate should have B.E/B.Tech Degree in Electronics/Electronics & Communication/Electronics & Telecommunication Engineering with minimum 55% marks from a recognized Institute/ University.  He/She should have <b>minimum 06 years</b> of post-qualification industrial experience (minimum 04 years of post qualification industrial experience for candidates having M.E/M.Tech. Degree) in design and development of Microwave components, sub-systems, systems etc.  OR Candidates with Ph.D. in the relevant area without

		<p>Facility i.e. ATE etc.</p> <p>(iv) Candidate will also be responsible for providing life time support for Microwave products in terms of obsolescence management, design upgradation etc.</p>	<p>any post qualification experience will be eligible for the post.</p> <p><b>Preferable:</b></p> <p>(i) Proficiency in working on Design and simulation 2D/3D tools like ADS, HFSS, AWR, etc.</p> <p>(ii) Proficiency in working on mathematical programming language like MATLAB etc.</p> <p>(iii) Experience in handling of Microwave components/Devices.</p> <p>(iv) Exposure to programming techniques including FPGA etc.</p> <p>(v) Should have work experience in research/ development/ production of defence related products.</p> <p>(vi) Knowledge on defence and airborne standards, documentation and testing as per customer requirement covering active phased array radar applications etc.</p>
<p><b>3</b></p>	<p><b>Assistant Technical Manager (SPD)</b></p> <p>Grade – E2 Post-03 01 (UR) 01 (OBC) 01 (EWS)</p>	<p>The candidate shall be responsible for dealing with embedded system and hardware design, design and testing of PCB and electronic circuits.</p>	<p>The candidate should have BE/B.Tech Degree in Electronics/Electronics &amp; Communication/ Electronics &amp; Telecommunication/Electronics &amp; Instrumentation Engineering with minimum 55% marks from a recognized Institute/University.</p> <p>He/She should have <b>minimum 06 years</b> of post qualification industrial experience in embedded system and hardware design; designing, testing and simulation of electronics circuits; designing &amp; testing of printed circuit board (PCB) and ability to involve and carry out documentation throughout the product development lifecycle.</p> <p><b>Desirable:</b> Preference will be given to candidates having the following certificates :</p> <p>(i) An ISO 9001:2015 certified auditor for quality management system.</p> <p>(ii) An ISO 22163 certified auditor (Indian Railway International Standards) for quality management systems in the rail industry.</p> <p>(iii) Certification in Six Sigma Green Belt.</p>

4	<p><b>Deputy Engineer (SPD)</b></p> <p>Grade – E1 Post-04 01 (SC) 01 (ST) 01 (OBC) 01 (EWS)</p>	<p>The candidate shall be responsible for dealing with embedded system and hardware design, design and testing of PCB and electronic circuits.</p>	<p>The candidate should have BE/B.Tech Degree in Electronics/Electronics &amp; Communication/ Electronics &amp; Telecommunication/Electronics &amp; Instrumentation Engineering with minimum 55% marks from a recognized Institute/University.</p> <p>He/She should have <b>minimum 02 years</b> of post qualification industrial experience in embedded system and hardware design; designing, testing and simulation of electronics circuits; designing &amp; testing of printed circuit board (PCB) and ability to involve and carry out documentation throughout the product development lifecycle.</p> <p style="text-align: center;">OR</p> <p>Candidates, who have passed M.E./M.Tech Degree in Electronics Engineering/Integrated Embedded Systems (without any post qualification experience) will be eligible for the post.</p> <p><b>Desirable:</b> Preference will be given to candidates having the following certificates :</p> <p>(i) An ISO 9001:2015 certified auditor for quality management system. (ii) An ISO 22163 certified auditor (Indian Railway International Standards) for quality management systems in the rail industry. (iii) Certification in Six Sigma Green Belt.</p>
5	<p><b>Deputy Engineer (Microwave)</b></p> <p>Grade – E1 Post-02 01 (ST) 01 (UR)</p>	<p>The candidate shall be responsible for the following:</p> <p>(i) Primarily for Design, Development, assembly &amp; testing of microwave components, sub-system &amp; system.</p> <p>(ii) Candidate will also be part of a team for Setting-up of assembly (SMT line /RF assembly line) &amp; testing Facility.</p> <p>(iii) Candidate will also be responsible for providing life time support for Microwave products in terms of obsolescence management, design upgradation etc.</p>	<p>The candidate should have B.E/B.Tech Degree in Electronics / Electronics &amp; Communication/ Electronics &amp; Telecommunication Engineering with minimum 55% marks from a recognized Institute/ University.</p> <p>He/She should have <b>minimum 02 years</b> of post-qualification industrial experience in design and development of Microwave components, sub-systems and systems etc.</p> <p style="text-align: center;">OR</p> <p>Candidates with M.E./M.Tech. Degree in relevant area (without 02 years post qualification experience) will be eligible for the post.</p> <p><b>Preferable:</b></p> <p>(i) Proficiency in working on Design and simulation 2D/3D tools like ADS, HFSS, AWR, etc. (ii) Proficiency in working on mathematical programming language like MATLAB etc. (iii) Experience in handling of Microwave components/Devices (iv) Exposure to programming techniques including FPGA etc. (v) Knowledge on defence and airborne standards, documentation and testing as per customer requirement covering active phased array radar applications etc.</p>

6	<b>Deputy Engineer (MED)</b>  Grade – E1 Post–01 (SC)	The Candidate shall be responsible for the following: (i) Manufacturing, supervision and production planning for day to day activities. (ii) Preparation of the routine reports and documentation management. (iii) Maintaining, monitoring and ensuring the highest quality in products, tabulating and documenting data related to processes, material, product qualities and reliabilities.	The candidate should have B.E./B.Tech. Degree in Mechanical Engineering with minimum 55% marks from a recognized Institute/ University.  He/She should have <b>minimum 2 years</b> post qualification experience in quality control, should have knowledge of quality tools and application of quality tools.  The candidate should have knowledge of component design using AUTOCAD, SOLIDWORKS and Knowledge of General dimensions & Tolerances, mechanical & production drawings.
7	<b>Deputy Engineer (Civil)</b>  Grade – E1 Post–02 01 (UR) 01 (OBC)	The Candidate shall be responsible for rehabilitation, retrofitting and strengthening works of old buildings. He/ she shall be responsible for tendering, billing and documents preparation and dealing with vendors.	The candidate should have BE/B. Tech Degree in Civil engineering with minimum 55% marks from a recognized Institute / University.  He/ She should have <b>minimum 02 years</b> of post qualification experience in execution of civil work. The candidate should have knowledge of methodology and operating procedure of rehabilitation, retrofitting and strengthening works.  <b>Desirable</b> M.E./M.Tech Degree in civil engineering from recognized Institute/ University.
8	<b>Deputy Engineer (SSG) on Contract Basis</b>  Grade – E1 Post–02 01 – OBC 01 - UR	The candidate shall be responsible for Project Management for Security & Surveillance/Telecom/ICT/ Tenders/Projects.	The candidate should have B.E./B.Tech. Degree in Electronics/Telecommunication/ Computers/ Electrical/ Mechanical engineering or equivalent qualification with minimum 55% marks from a recognized Institute/University.  He/She should have <b>minimum 02 years</b> of post qualification experience in marketing/project management of Security Surveillance Systems, IT/Networking solution and should be ready to work at sites also.  Preference will be given to candidate with MBA/ M.Tech/PG Diploma in Marketing Management from a recognized institute/university.
9	<b>Deputy Engineer (IT) on Contract Basis</b>  Grade – E1 Post–02 01 – OBC 01 - UR	The candidate shall be responsible for all the technical works related to the IT in CEL. The work may include configurations of switches, maintaining of IP phones along with server.  IP maintenance will also be the part of the responsibilities.	The candidate should have B.E./B.Tech. Degree in Computer Science with minimum 55% marks from recognized Institute/University.  He/She should have <b>minimum 02 years</b> of post qualification experience in IT/Networking solution and ready to work at sites also.

10	<b>Deputy Engineer (MED) on Contract Basis</b>  Grade – E1 Post–02 01 – OBC 01 - UR	The candidate shall be responsible for the following: (i) Development activities & experimentation in advanced/functional ceramics and materials domain. (ii) Production and Testing of advanced ceramics. (iii) Preparation of the routine reports and documentation management.	The candidate should have B.E./B.Tech. Degree in Metallurgy/Materials Science/Ceramic Engineering with minimum 55% marks from a recognized Institute/ University.  He/She should have <b>minimum 02 years</b> post qualification experience relevant to above fields of study.  OR Candidates, who have passed M.E/M.Tech Degree in Metallurgy/Materials Science/Ceramic Engineering or equivalent disciplines (without 02 years post qualification experience) will be eligible for the post.  <b>Desirable:</b> (i) Preference will be given to candidates with prior experience in research laboratories or industries. (ii) Preference will be given to candidates with knowledge of advanced characterization techniques of materials. (iii) He/She should have excellent written, verbal communication, presentation skills and proficiency in word, excel and power point. (iv) Should be highly organized, self-driven and be able to handle multiple tasks simultaneously.
11	<b>Graduate Engineer Trainee (Mechanical) on contract basis (consolidated pay)</b>  Post–03 (UR)	The candidate shall be responsible for the following: (i) Design of mechanical parts and fixtures required for production. (ii) Manufacturing supervision and production planning for day to day activities. (iii) Preparation of the routine reports and documentation management.	The candidate should have B.E./B.Tech. Degree in Mechanical Engineering with minimum 55% marks from a recognized Institute/ University.  The candidate should have knowledge of component design using AUTOCAD, SOLIDWORKS.  Candidate should have knowledge of General dimensions & Tolerances, mechanical & production drawings.
12	<b>Graduate Engineer Trainee (Metallurgy/ Materials Science/ Ceramic) on contract basis (consolidated pay)</b>  Post: 04 (UR)	The candidate shall be responsible for the following: (i) Development activities & experimentation in advanced/functional ceramics and materials domain. (ii) Production and Testing of advanced ceramics. (iii) Preparation of the routine reports and documentation management.	The candidate should have B.E./B.Tech. Degree in Metallurgy/Materials Science/Ceramic Engineering with minimum 55% marks from a recognized Institute/ University.

13	<b>Graduate Engineer Trainee (ECE) on contract basis (consolidated pay)</b> Post: 01 (UR)	The candidate shall be responsible for the following: (i) Production and maintenance activities. (ii) Understanding the electronic circuit and problem diagnosis.	The candidate should have B.E./B.Tech. Degree in Electronics and Communication Engineering with minimum 55% marks from a recognized Institute/ University.
----	--	---	--

The Scale of Pay in the scale and age limit are as under:

Grade/ Post	Pay Scale/ Total Emolument	Approx CTC for Regular post	Approx CTC for Contractual post	Age Limit As on 31.05.2024
E4/ Sr. Manager	Rs. 70000-3%-200000	Rs.18.97 Lacs p.a.	--	42 years
E3/Manager	Rs. 60000-3%-180000	Rs.16.32 Lacs p.a.	--	38 years
E2/ Assistant Technical Manager	Rs.50000-3%-160000/-	Rs.13.67 Lacs p.a.	--	35 years
E1/ Deputy Engineer	Rs.40000-3%-140000/-	Rs.11.02 Lacs p.a.	Rs.09.59 Lacs p.a.	30 years

OR

Post Name	Consolidated Pay (per month)	Age Limit As on 31.05.2024
<b>Graduate Engineer Trainee on contract basis* (Consolidated Pay)</b>	Rs.35,000/- in 1 <sup>st</sup> Year Rs.40,000/- in 2 <sup>nd</sup> Year	<b>28 years</b>

*\*The incumbent will be engaged initially for a period of two years. On completion of two years tenure, on requirement basis, he/she may be placed on pay scale basis of E-1 Grade on contract basis.*